IN THE DRAWINGS

Please replace the drawings with the enclosed drawings.

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I/O

DIGITAL VOCODER 120

TRANSMIT MODULATOR 122

124

DIVERSITY COMBINER/DECODER 116

CONTROL PROCESSOR 118

RAM 126

SEARCHER RECEIVER 110

DIGITAL DATA RECEIVER 112

DIGITAL DATA RECEIVER 114

DIGITAL DATA RECEIVER 115

ANALOG RECEIVER 106
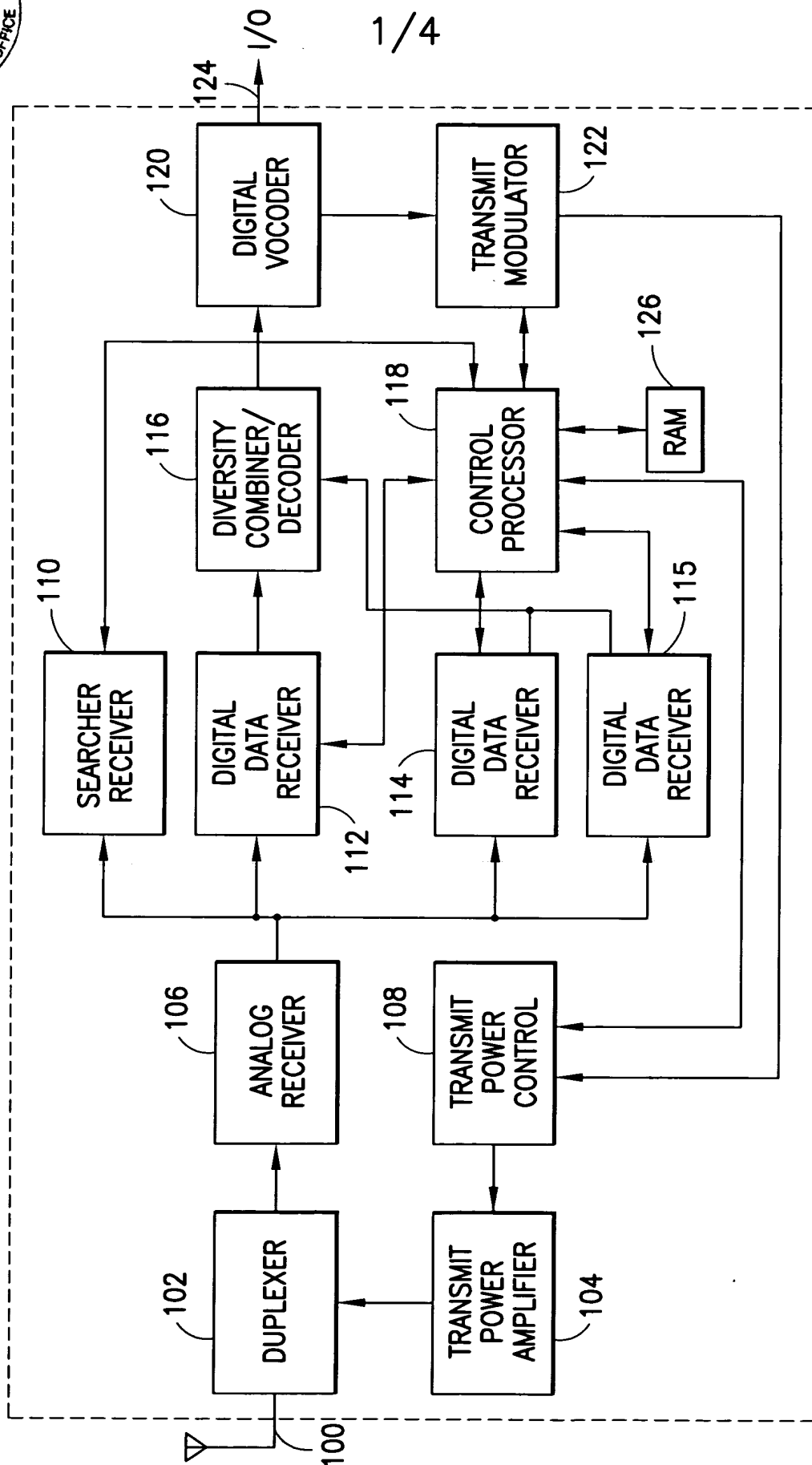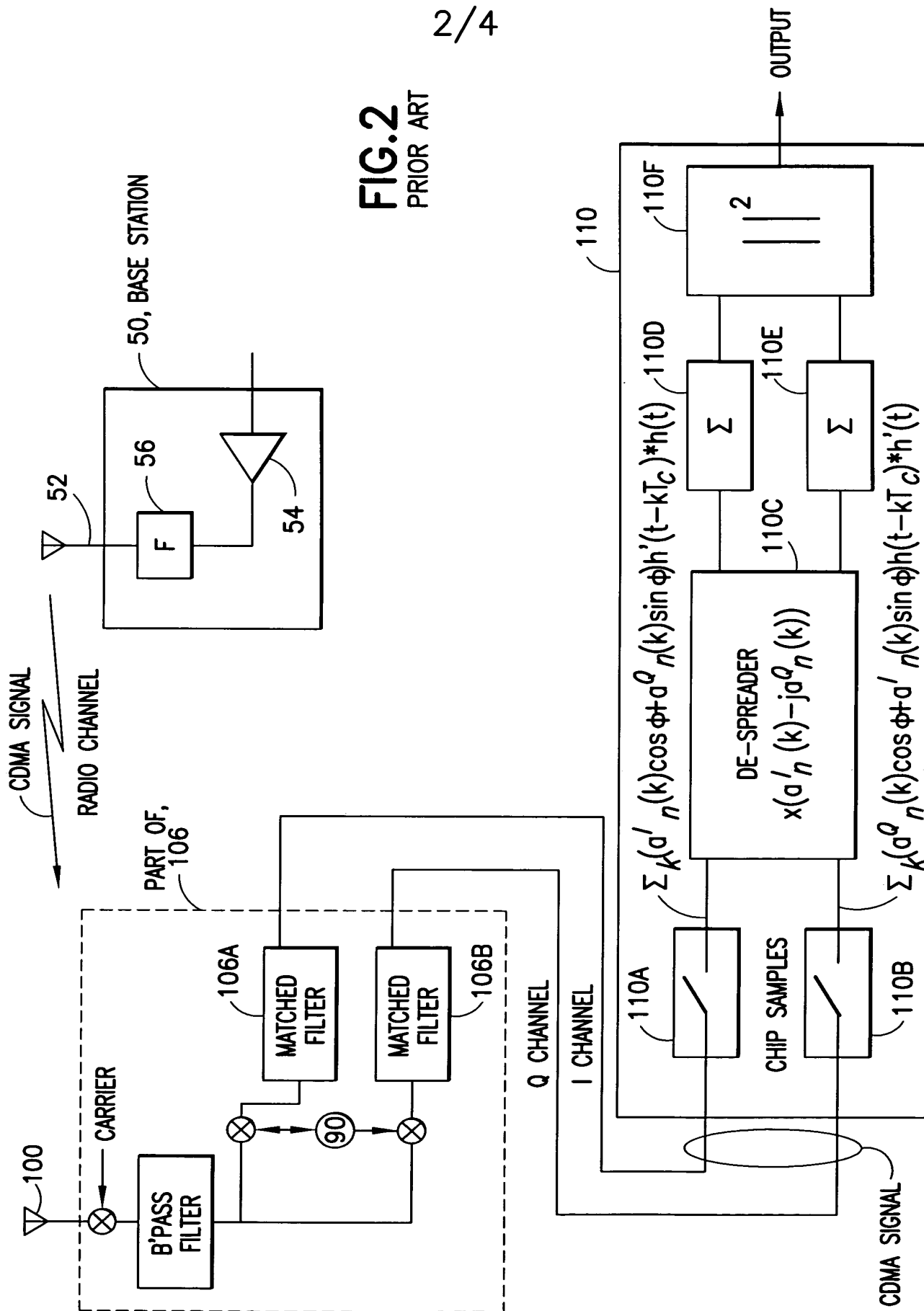
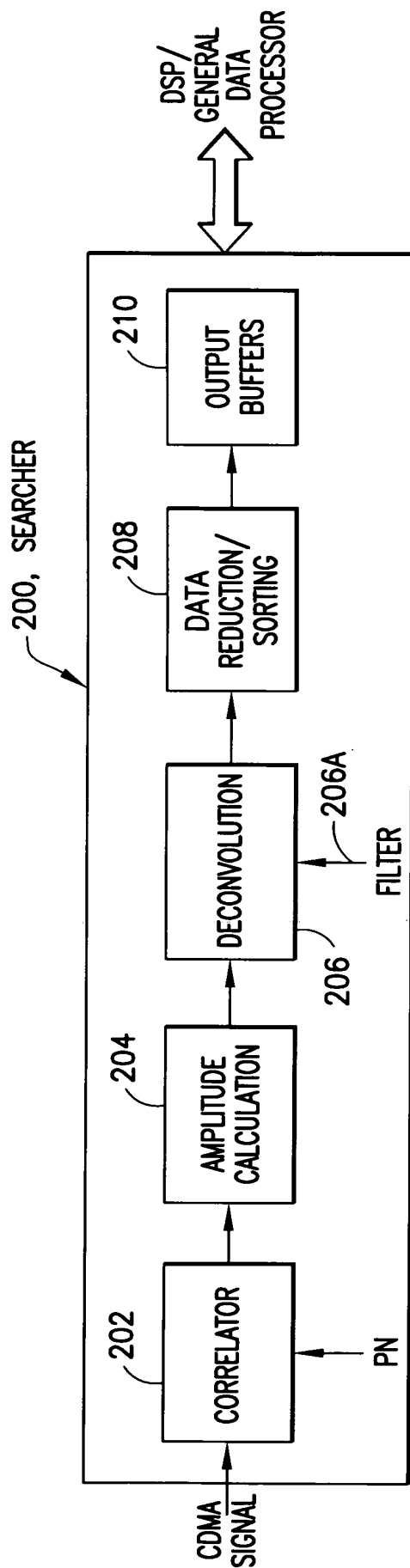TRANSMIT POWER CONTROL 108

DUPLEXER 102

TRANSMIT POWER AMPLIFIER 104

100

130

FIG.1

FIG.2
PRIOR ART

FIG.3A

FIG.3B
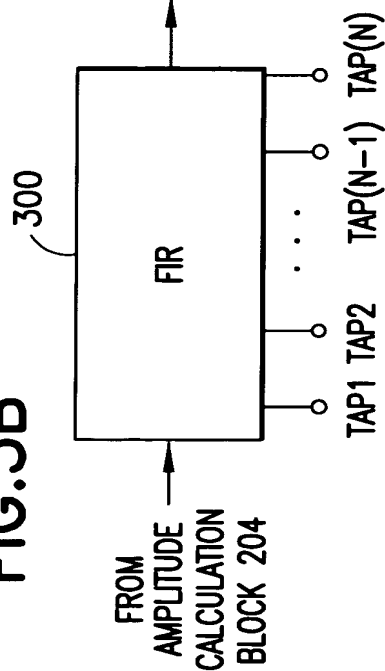
FIG.3C
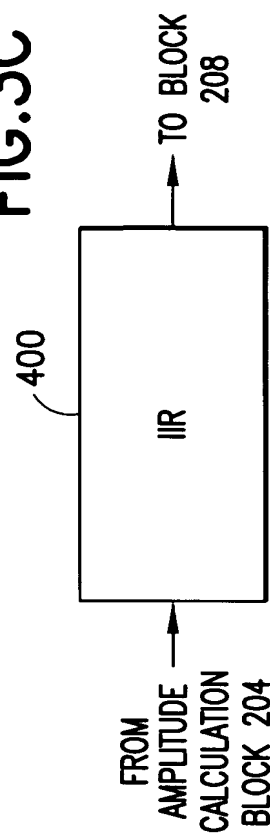
FIG.3D

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FIG.4
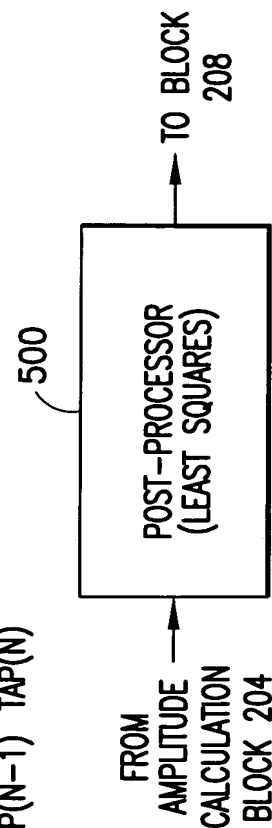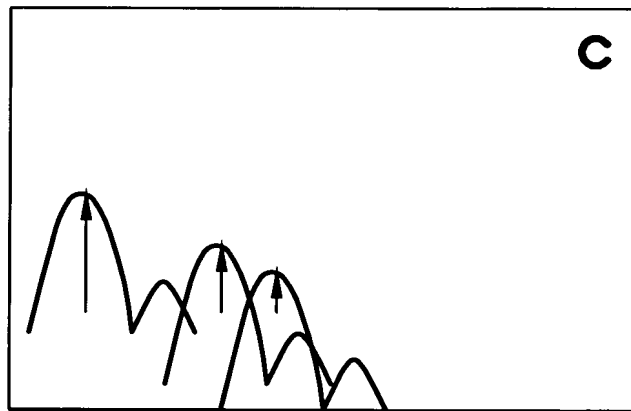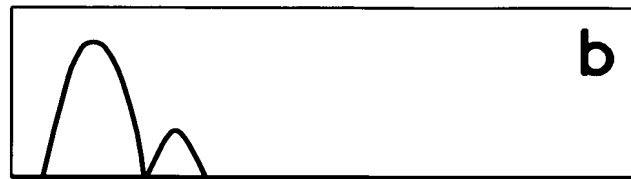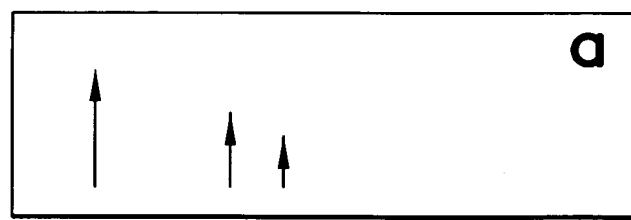

$$F = \begin{bmatrix} fk & \dots & f2k+1 & 0 & 0 & \dots & 0 & 0 & 0 \\ fk-1 & \dots & f2k & f2k+1 & 0 & \dots & 0 & 0 & 0 \\ fk-2 & \dots & f2k-1 & f2k & f2k+1 & \dots & 0 & 0 & 0 \\ \dots & \dots & \dots & \dots & \dots & \dots & \dots & \dots & \dots \\ 0 & \dots & 0 & 0 & 0 & f0 & \dots & fk-1 & fk \end{bmatrix}$$

FIG.5